



500.40681X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. OHYANAGI, et al.
Application No.: 09/964,809
Filed: September 28, 2001
For: FIELD EFFECT TRANSISTOR AND SEMICONDUCTOR
DEVICE MANUFACTURING METHOD
Art Unit: 2818
Examiner: R. BERRY

RESPONSE TO ELECTION REQUIREMENT

MS: NON-FEE AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 24, 2003

Sir:

In response to the Restriction Requirement dated November 24, 2003, Applicants hereby elect the **Group I** claims 1-5, directed to a field effect transistor, without traverse.

To the extent necessary, applicants petition for an extension of time under 37 CFR §1.136. Please charge any shortage in the fees due in connection with the filing of this paper, to the Deposit Account of Antonelli, Terry, Stout & Kraus, LLP, Dep. Acct. No. 01-2135 (500.40681X00), and please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By

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Patent Dkt. No. 500,40681X00 Attorney Initials GEM/dlt

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Applicant(s) T. OHYANAGI, et al.

Papers Filed Herewith on December 24, 2003

Receipt is hereby acknowledged of the papers filed as indicated by the checked items in connection with the above-identified application:

New Application Transmittal Form Credit Card Payment Form
 Pages of Specification (claims) Fees \$
 Sheets of Drawings Amendment
 Declaration (pages) Response to Office Action
 Fee Transmittal Form Petition for Extension of Time (mos.)
 Claim for Priority Information Disclosure Statement
 Priority Documents PTO-1449 Copies of References
 Assignment Papers Revised Drawings (sheets)
 Issue Fee Transmittal Notice of Appeal
 Other

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